

2015 MIMS TRAINING CALENDAR



JANUARY

S	M	T	W	T	F	S
				1	2	3
4	5	6	7	8	9	10
11	12	13	14	15	16	17
18	19	20	21	22	23	24
25	26	27	28	29	30	31

FEBRUARY

S	M	T	W	T	F	S
1	2	3	4	5	6	7
8	9	10	11	12	13	14
15	16	17	18	19	20	21
22	23	24	25	26	27	28

MARCH

S	M	T	W	T	F	S
1	2	3	4	5	6	7
8	9	10	11	12	13	14
15	16	17	18	19	20	21
22	23	24	25	26	27	28
29	30	31				

APRIL

S	M	T	W	T	F	S
			1	2	3	4
5	6	7	8	9	10	11
12	13	14	15	16	17	18
19	20	21	22	23	24	25
26	27	28	29	30		

MAY

S	M	T	W	T	F	S
				1	2	
3	4	5	6	7	8	9
10	11	12	13	14	15	16
17	18	19	20	21	22	23
24	25	26	27	28	29	30
31						

JUNE

S	M	T	W	T	F	S
1	2	3	4	5	6	
7	8	9	10	11	12	13
14	15	16	17	18	19	20
21	22	23	24	25	26	27
28	29	30				

JULY

S	M	T	W	T	F	S
			1	2	3	4
5	6	7	8	9	10	11
12	13	14	15	16	17	18
19	20	21	22	23	24	25
26	27	28	29	30	31	

AUGUST

S	M	T	W	T	F	S
						1
2	3	4	5	6	7	8
9	10	11	12	13	14	15
16	17	18	19	20	21	22
23	24	25	26	27	28	29
30	31					

SEPTEMBER

S	M	T	W	T	F	S
			1	2	3	4
5	6	7	8	9	10	11
12	13	14	15	16	17	18
19	20	21	22	23	24	25
26	27	28	29	30		

OCTOBER

S	M	T	W	T	F	S
				1	2	3
4	5	6	7	8	9	10
11	12	13	14	15	16	17
18	19	20	21	22	23	24
25	26	27	28	29	30	31

NOVEMBER

S	M	T	W	T	F	S
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8	9	10	11	12	13	14
15	16	17	18	19	20	21
22	23	24	25	26	27	28
29	30					

DECEMBER

S	M	T	W	T	F	S
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5	6	7	8	9	10	11
12	13	14	15	16	17	18
19	20	21	22	23	24	25
26	27	28	29	30	31	

23 & 24 March
Advanced Signal Integrity : Mastering High Speed Serial Data Technology by Ramson Stephens (PhD), Venue: PENANG

25 - 27 March
Introduction to Board Level Signal Integrity Simulation , Venue: SHRDC

7 - 10 April
IPC-CID Certified Interconnect Designer Workshop and Exam (PCB Designer), Venue: SINGAPORE

5 - 8 May
IPC-CID Certified Interconnect Designer Workshop and Exam (PCB Designer) Venue: PENANG

18 & 19 May
IC Design: Rapid Systems Development using Zynq® All Programmable SOC Platforms (with hands-on FPGA kit in class project) Venue: PENANG/SHRDC

8 - 10 June
Introduction to Board Level Signal Integrity Simulation Venue: SHRDC

29 - 31 July
IC Design: Custom analog IC layout for Beginners using Virtuoso Venue: SHRDC

August
Advanced Signal Integrity : Understanding Jitter and Error Analysis by Ramson Stephens (PhD) Venue: PENANG

4,5 & 6 August
Custom Analog & Mixed Signal Integration for System on a Chip Venue: PENANG/SHRDC

18 - 21 August
IPC-CID Certified Interconnect Designer Workshop and Exam (PCB Designer) Venue: SINGAPORE

8 - 11 September
IPC-CID+ Advanced Certified Interconnect Designer Workshop and Exam (PCB designer) Venue: PENANG

20-23 Oct (SG)
IPC-CID+ Advanced Certified Interconnect Designer Workshop and Exam (PCB designer) Venue: SINGAPORE

Note: Course fees published on website is subject to change without notice.

we grow talents...

For more details on the courses, hover your CURSOR over the subject of your interest.

To register, CLICK or SCAN the QR Code below:



Email us for further enquiries!

Updated: 10th December 2014

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